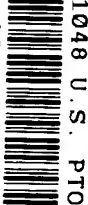


10/31/01



1048 U.S. PTO

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Docket No.: 11587 M-12336 US

10/31/01
10/002696
1048 U.S. PTO

October 31, 2001

Box Patent Application
Commissioner For Patents
Washington, D. C. 20231

I enclosed herewith for filing is a patent application, as follows:

Inventor(s): Eliyahou Harari, George Samachisa, Jack H. Yuan and Daniel C. Guterman
 Title: Multi-State Non-Volatile Integrated Circuit Memory Systems That Employ Dielectric Storage Elements

X Return Receipt Postcard
X This Transmittal Letter (in duplicate)
22 page(s) Specification (not including claims)
6 page(s) Claims
1 page Abstract
5 Sheet(s) of Drawings
2 page(s) Declaration For Patent Application

CLAIMS AS FILED


For	Number Filed		Number Extra		Rate		Basic Fee
Total Claims	25	-20	=	5	x	\$18.00	= \$ 90.00
Independent Claims	6	-3	=	3	x	\$84.00	= \$ 252.00
<input checked="" type="checkbox"/>	Fee of \$280.00 for the first filing of one or more multiple dependent claims per application						\$ 280.00
<input type="checkbox"/>	Fee for Request for Extension of Time						\$

Please make the following charges to Deposit Account 19-2386:

- ☒ Total fee for filing the patent application in the amount of \$ 1,362.00
☒ The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account 19-2386.

**EXPRESS MAIL
 LABEL NO:
 EL707911884US**

Respectfully submitted,


 Gerald P. Parsons
 Reg. No. 24,486

Date

Oct. 31, 2001